GSI TECHNOLOGY

Creator of the Associative Processing Unit (APU) for AI and a leading provider of high-performance memory solutions

Lee-Lean Shu, Chairman, President & CEO
Doug Schirle, CFO
Didier Lasserre, VP of Sales and Investor Relations

August 2026

SAFE HARBOR

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GSI BY THE NUMBERS

LTM Revenue (as of Q1 FY2027)
$25.1M

Cash and cash equivalents[1]
$77.0M
(NO DEBT)

Market Cap[2]
$258.3M

Insider Ownership
20%

Years with TSMC
30

SRAM chips shipped to date
140M

Employees Worldwide
126

Patents Granted
158 Total
96 for APU

Founded / IPO
1995 / 2007

1. Includes cash and cash equivalents, short and long-term investments as of June 30, 2026.
2. Based on the closing share price of $6.72 on August 7, 2026, and common stock outstanding of 38,432,590 shares as July 31, 2026.

FROM SRAM LEADER TO EDGE AI COMPUTE PLATFORM

TRANSITION INTO EDGE AI ENABLED BY 30+ YEARS OF SEMICONDUCTOR EXPERTISE

Leveraging Decades of Memory Innovation to Build the Optimal Edge AI Processor

High-Performance SRAM Leadership

- Industry leader in high-density SRAM
- Cash from operations funds APU
- 30 years of collaboration with TSMC
- 140 million SRAM chips shipped

Developer of APU Compute-in-Memory (CIM)

- 11+ years of R&D
- $175M invested
- 96 APU patents
- Proprietary CIM architecture

Emerging Edge AI Platform Leader

APU Families

- Gemini-II
- Plato
- Future APUs

WHY EDGE AI REQUIRES A DIFFERENT PROCESSOR

Cloud AI Processors

- Deliver exceptional AI performance, but are designed for data centers, not edge devices
- Require substantial power and cooling infrastructure
- Depend on moving data to and from the cloud, creating latency, bandwidth, and security challenges

Conventional Edge Processors

- Fit within edge power budgets, but often cannot deliver the AI performance required for real-time decision making
- Higher-performance alternatives typically consume too much power for many edge deployments
- Force tradeoffs between performance, power consumption, and response time

Edge AI's future will be defined not by maximum compute, but by maximum performance per watt

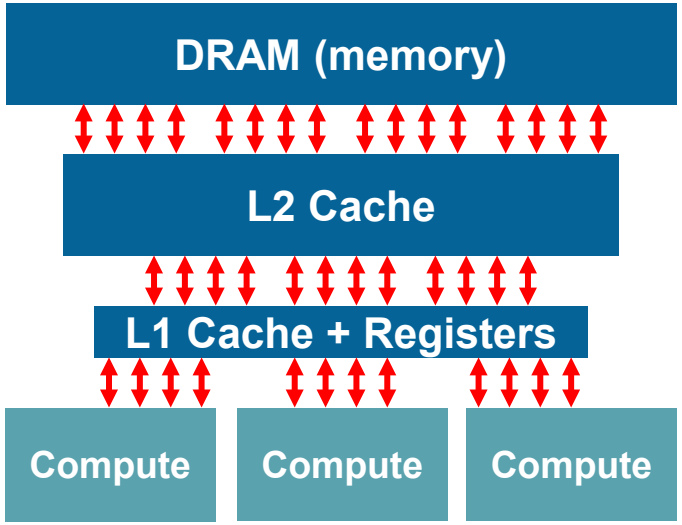
CIM REDUCES DATA MOVEMENT, USES LESS POWER

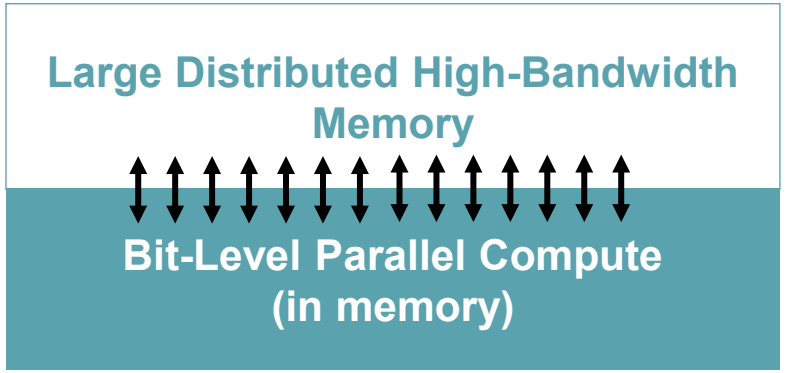
GPU, CPU (Von Neumann model)

- **Massive data transfers**
- **Higher power consumption / more heat created**

APU CIM

- **Limited data movement**
- **Lower power consumption / less heat generated**



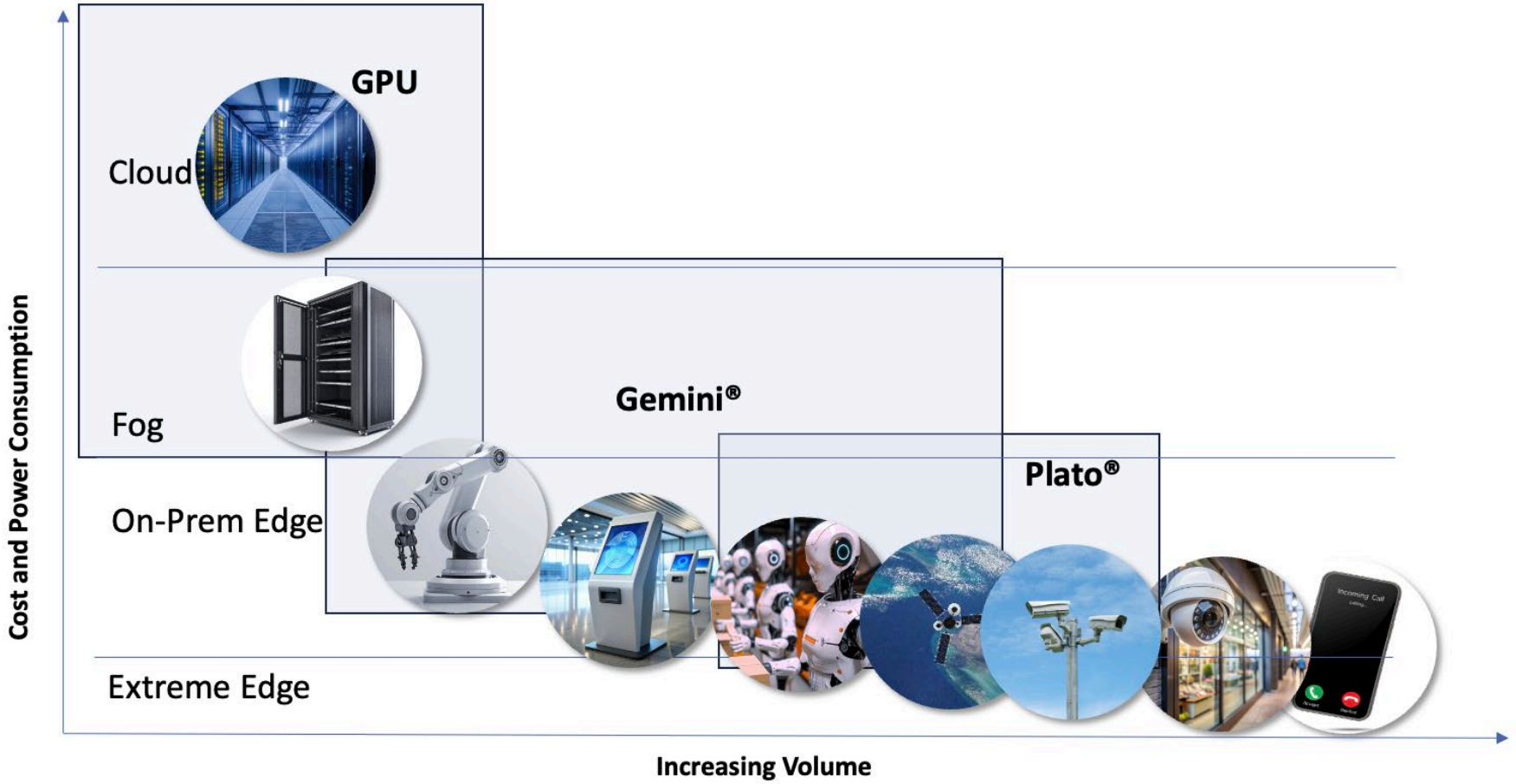


Up to ~98% lower energy consumption compared with an NVIDIA A6000 in Cornell RAG testing*

*Source: "Characterizing and Optimizing Realistic Workloads on a Commercial Compute-in-SRAM Device," Cornell University-led research collaboration publication (2025) Demonstrated in Cornell-led research on RAG workloads versus NVIDIA A6000 using GSI's CIM architecture

TARGETED EDGE APPLICATIONS FOR GSI APU FAMILY

NOT EVERY AI WORKLOAD BELONGS IN THE CLOUD



GEMINI-II IN THE "SWEET SPOT" FOR EDGE AI

BAKE-OFF DEMONSTRATED BEST PERFORMANCE-PER-WATT

Platform	Time to First Token (TTFT)	System Power
Gemini-II APU	**< 2.5 seconds**	**~30W**
Snapdragon X Elite	12 seconds	~30W
Jetson Thor	~ 3 seconds	>100W

Gemini-II delivers up to 300% better performance-per-watt with fastest response time

(Sources: company testing and publicly reported benchmark results for Gemma-3 12B multimodal model (video + text inputs))

GEMINI-II POCS: DEMONSTRATE REPEATABLE EDGE AI PLATFORM

Current Platform Validation in Ongoing POCs

- Defense/Tactical Edge: ”Sentinel” perimeter-security drone
- Smart Infrastructure: ”Smart Guardian Eye” smart-city deployment

Adjacent Market Opportunities for Future POCs

- Industrial inspection
- Robotics
- Autonomous systems
- Commercial drones

Each deployment strengthens a reusable platform that accelerates expansion into adjacent Edge AI markets

* From Grandview Research press release dated May 2025: Smart City Market Growth & Trends

GEMINI-II: TARGETING MARKETS WHERE PERFORMANCE-PER-WATT MATTERS MOST

SELECTED MARKETS SHARE THE POWER, LATENCY, AND REAL-TIME DECISION-MAKING REQUIREMENTS WHERE GEMINI-II IS MOST DIFFERENTIATED

Target Edge AI Markets

- Smart Infrastructure
- Autonomous Drones
- Defense & Autonomous Systems
- Satellite & Space AI
- Industrial Vision

Ongoing Program Validation

- Taiwan Smart City POC
- “Sentinel” POC
- “Sentinel” POC; Army SBIR
- YOLO SBIRs / Radiation tested Gemini-II SBIR
- Natural expansion from current POCs

Targeting Edge AI markets where Gemini-II’s advantages can accelerate commercial adoption

PLATO: EXTENDING EDGE AI TO AUTONOMOUS SYSTEMS

DESIGNED TO BRING MULTIMODAL AI AWARENESS AND AUTONOMOUS DECISION-MAKING TO POWER-CONSTRAINED EDGE DEVICES

Gemini-II

- 15 W–100 W power profile
- Real-time multimodal AI
- Optimized for Smart Infrastructure
- Defense & Tactical Edge
- Industrial Vision
- Early autonomous systems

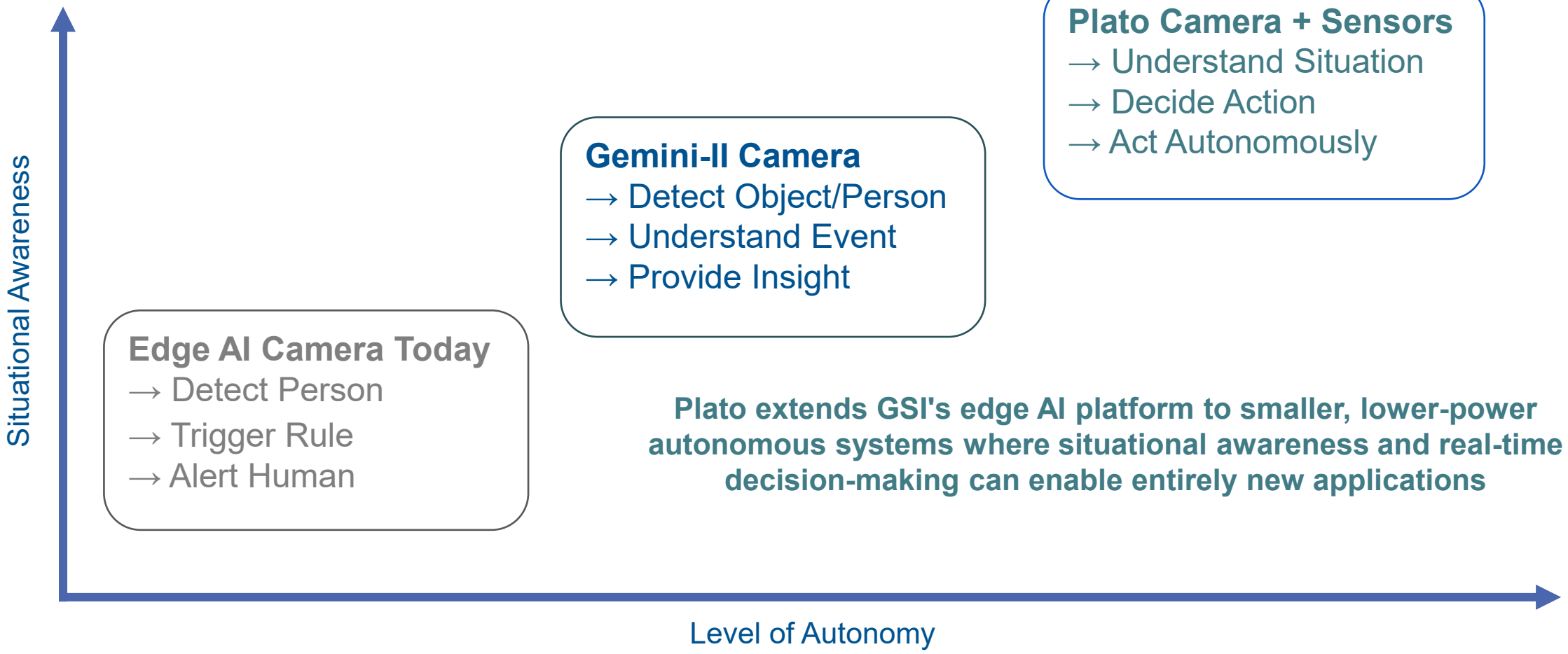
Plato—Next Generation APU

- Target 2 W–10 W power profile
- 1/4 the footprint of Gemini-II
- Higher memory bandwidth
- Support for larger multimodal AI models
- Designed for embedded autonomous systems

Real-time Inference

Context-Aware AI

PLATO: ENABLING CONTEXT-AWARE AUTONOMOUS EDGE AI



Plato extends GSI's edge AI platform to smaller, lower-power autonomous systems where situational awareness and real-time decision-making can enable entirely new applications

APU ROADMAP

START WITH SPECIALIZED EDGE AI DEPLOYMENTS; EXPAND INTO BROADER AI MARKETS

CY2026 Gemini-II Deployments
Specialized Edge AI

CY2028 Plato Launch
Broader Edge LLM

Future Enhanced APUs
Expanding Edge Targets

Deployments

- "Sentinel" G2 Tech Drone POC
- Taiwan "Smart Guardian Eye"—Real-time alerts from surveillance cameras
- US Army Phase-II SBIR Ruggedized Edge Platform

Highlights

- Higher DRAM bandwidth for VLM
- 10-watt power profile
- 1/4 the size of Gemini-II footprint
- Small form factor will expand edge applications

BUILDING A SCALABLE EDGE AI PLATFORM

LEVERAGING REUSABLE SOFTWARE, AI-ASSISTED DEVELOPMENT TOOLS, AND SYSTEM INTEGRATOR PARTNERSHIPS TO EXPAND ACROSS MULTIPLE EDGE AI MARKETS

Current Commercialization Strategy

- Focus on a small number of high-value POCs
- Validate Gemini-II in real-world deployments
- Reuse software, models, and tools across applications
- Expand into adjacent edge AI markets

Building a Repeatable Growth Engine

- AI-assisted SDK accelerates application development
- Adapt software developed for one application
- Support opportunities without growth in engineering resources
- Scale Gemini-II deployments through system integrators
- Grow design-win pipeline while maintaining lean operating model

FINANCIAL OVERVIEW—Q1 FY2027

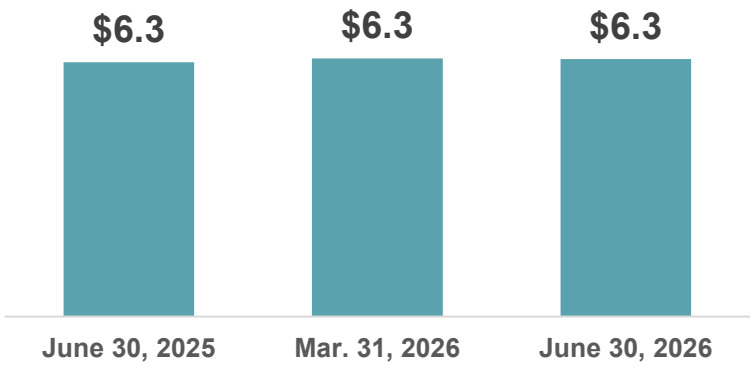
SUFFICIENT FUNDS TO COMMERCIALIZE GEMINI-II & ADVANCE PLATO DESIGN

Stable SRAM revenue from continued demand from GSI's largest customers supporting leading AI processor platforms and solid military/defense customers.

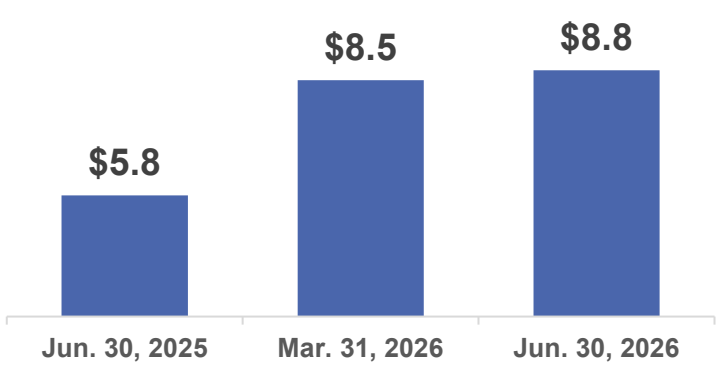
Q1 FY2027 includes higher R&D related to Plato chip design and associated chip design consulting fees, as well as finalization of Gemini-II software for ongoing POC and projects.

Cash from financing activities in Q1 FY'27 were $14.0M including $9.3M in ATM proceeds. Cash used in operating activities was $3.9M, related to Plato development and support of Gemini-II software development.

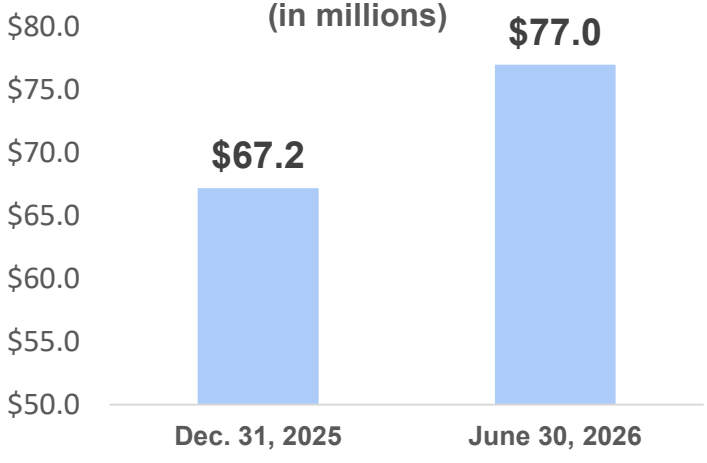
Quarterly Net Revenues
(in millions)



Operating Expenses
(in millions)

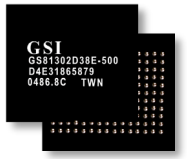


Cash & Cash Equivalents
(in millions)



HIGH-MARGIN SRAM BUSINESS FUNDS APU DEVELOPMENT

SRAM REVENUE PROVIDES FINANCIAL FOUNDATION TO DEVELOP AND COMMERCIALIZE APU PLATFORM



High density, low latency, high bandwidth, low power consumption

- Industry leader with largest portfolio of high-performance memory products
- SigmaQuad™ and SigmaDDR™ core business growth drivers
- SigmaQuad™ SRAMs recognized for industry-leading density and speeds
- 3rd and 4th Generation, sole-sourced, fastest off-the-shelf SRAM on market

RADIATION-HARDENED & TOLERANT SRAM

FOR AEROSPACE, SATELLITE & DEFENSE

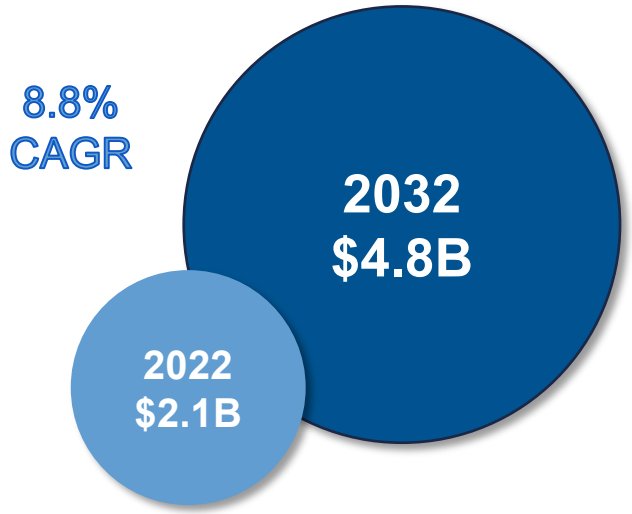
Advantages:

- Chips designed to protect from solar, deep space radiation
- Highest density, fastest speed, with “pin-to-pin” replacement
- Completed testing and stringent qualification processes

Future revenue and profitability drivers:

- Awaiting shipment on received orders
- ASP up to $30K with 90%+ gross margin
- Recurring revenue lifecycles of 10+ years; long design cycles

Space Semiconductor TAM
Radiation Hardened, Radiation Tolerant



Allied Market Research February 2024

Shared defense customer ecosystem supports APU commercialization.

INVESTMENT HIGHLIGHTS

BUILDING A SCALABLE EDGE AI PLATFORM COMPANY

- **Proven semiconductor manufacturing model** capable of supporting future APU growth
- **Strong financial foundation** with $77.0M cash and no debt
- **Gemini-II validation** through active Smart City, Defense, and U.S. Army programs
- **Gemini-II chip production-ready**; AI-assisted SDK expected this fall
- **Building a scalable commercialization model** through reusable software, development tools, and system integrator partnerships
- **Plato extends the platform** into a new class of low-power autonomous edge AI systems

GSI TECHNOLOGY

GSI TECHNOLOGY

High Performance Components
for Leading-Edge Technology

GSITechnology.com / *IR Contact: GSIT@HaydenIR.com*

APPENDIX

ACCESSING DoW SBIRs & GOVERNMENT FUNDS

OFFSETTING R&D EXPENSES & CREATING APU AWARENESS WITHIN DoW AGENCIES

GSI awarded over $6 million of SBIR* funds to date

- $2 million Phase II with U.S. Army to develop & test ruggedized edge AI platform
- POC with G2 Tech along with DoW and a foreign defense agency valued at ~$1 million
- Space Development Agency Phase II SBIR valued at $751,000
- $1.25 million Space Development Agency Phase II SBIR
- $1.1 million Air Force Phase II SBIR
- $250K U.S. Army Phase I SBIR

Future DoW opportunities

- Phase 1 SBIR with Air Force worth $75K
- Additional SBIR proposals in queue for a total pipeline value of $6−$10 million

Pursuing new, larger DoW funding sources—AFWERX STRATFI/TACFI** programs for Gemini-II and Plato

Strategic funding from partnerships with potential customers

*SBIR – Small Business Innovation Research is a United States government program to stimulate technological innovation by funding small businesses to engage in federal R&D with the potential for commercialization. The DoW is one of the largest participants in the SBIR program.
** AFWERX is the innovation arm of the US Air Force, leveraging technology from military as well as private industry, academia and other government agencies.

APU PLATFORM & ROADMAP

Chip Launch Timing	2015–2026	CY 2026	CY2027
APU Roadmap	Gemini-I (28 nm)	Gemini-II (16 nm)	Plato (12 nm)
Target Applications	DB Index Builds SAR Fast Vector Search	Edge Search SAR “On-board” Computer Vision Multimodal LLMs HPC/Data Search	Edge AI, including: Multimodal GenAI/LLMs Computer Vision for • Drones • Automotive • Surveillance • Robotics
Libraries	Search Library (GSL) Vector Math	Radar Library (GRL) Vector Math Tensor Math	
Frameworks	C	Python	TensorFlow

EXPERIENCED MANAGEMENT TEAM

Name	Title	Years of Experience	Years with GSI	Prior Companies
Lee-Lean Shu	Chairman and CEO	47	30	Sony, AMD
Doug Schirle	Chief Financial Officer	47	26	Cypress, Pericom
Didier Lasserre	VP Sales and IR	38	28	Cypress, Solectron
Avidan Akerib	VP of Associative Computing	45	10	MikaMonu, NeoMagic
Patrick Chaung	SR VP of Memory Design	51	16	Sony, AMD
Bor-Tay Wu	VP of Taiwan Operations	45	29	Atalent, AMD

INCOME STATEMENT

CONDENSED CONSOLIDATED STATEMENTS OF OPERATIONS
(in thousands, except per share data)
(Unaudited)

SUMMARY BALANCE SHEET

CONDENSED CONSOLIDATED BALANCE SHEETS
(in thousands)
(Unaudited)